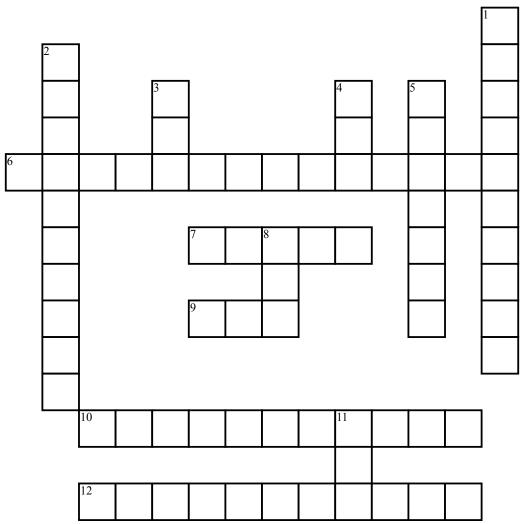
## Inside the CPU



## Across

- **6.** Stores the momory location of the next instruction to be executed
- **7.** Memory faster than RAM but slower than registers
- **9.** Stores the memory location for data that needs to be fetched from memory or stored in memory
- **10.** Stores the results of calculations made by the ALU

**12.** Decodes instructions and sends out signals to control how data moves around the CPU

## Down

- **1.** Carries control signals between CPU and memory
- **2.** Carries memory addresses for locations to be read from or written to
- **3.** Stores data that has been fetched or is waiting to be sent from memory

- **4.** Abbreviation of Central Processing Unit
- **5.** Carries data between the CPU and memory
- **8.** Stores the most recently fetched instruction while it is waiting to be decoded/executed
- **11.** Carries out calculations and logical decisions required by the program instruction

## **Word Bank**

ProgramCounter Cache Data Bus Address bus MDR ControlUnit Accumulator CIR

MAR CPU ALU Control Bus