

Name: \_\_\_\_\_ Date: \_\_\_\_\_ Period: \_\_\_\_\_

# Key Terms Lesson 1

W K E P O Y P C I D E C O M P O S I T I O N R C  
E F J R P B G E N K O R C R K E Y G N L D P P O  
B C R O H O E E T U U Y O E D N A U R E A V R M  
E L S P A O D C E B M V M H T C J A D D U J O P  
X C P E N L X O R E O E P I N P U T I J T C C O  
G O R R A E Q N F V D V U X C B H C G S H K E N  
G N I T L A A O A E E E T Q H B G R I A E I S E  
H C N Y O N B M C N L N A I A A L E T D N H S N  
H A T U G E S I E T I T T Y R C O A A A T D O T  
T T T I J X T C R H N R I E A K B T L P I E R S  
K E A N J P R I B A G R O Z C L A I D T C F H T  
M N S S C R A M I N A Z N T T O L V I I A A W N  
P A K X J E C P N D N K A D E G V E V V T U U A  
P T L H W S T A A L D L L I R U A E I E I L C R  
W E I I D S I C R E S Y T G U Z R X D T O T N G  
Q W S I Y I O T Y R I G H I E F I P E E N F N U  
T S T B N O N I P L M L I T V D A R W C F T F M  
E S A T F N U X I I U L N A A K B E J H Q M H E  
O U T P U T H H J K L I K L L K L S D N Z A X N  
P A R A M E T E R S A A I X U O E S N O V F J T  
F Z A H N B Z R F A T S N B A H J I T L I F Y S  
A W X U I D C F M Z I F G B T F L O D O F I U J  
E I L U T F Z Q R R O J D J E R E N A G J P D Z  
O D E F U R B E P F N K H G N D B B D Y I P M F

Modeling and simulation  
Boolean expression  
Digital Divide  
Abstraction  
Processor  
Property  
Backlog  
Input

Computational Thinking  
Sprint task list  
Authentication  
Concatenate  
Interface  
Evaluate  
Output  
Event

Adaptive technology  
Global variable  
Event handler  
Parameters  
Arguments  
Digital  
Analog

Creative expression  
Economic Impact  
Decomposition  
Components  
Character  
Default  
Binary