

Name: _____

Date: _____

Key terms lesson 1

N M S R O M N O I S S E R P X E E V I T A E R C
O Y B O O L E A N E X P R E S S I O N D V O J V
I R E L D N A H T N E V E V K N X A Q V O D I X
T T N E V E G M S T C C O M P O N E N T S S M O
A C W E U C H A R A C T E R U E Y U Q Z L A U Z
L O G N I K N I H T N O I T A T U P M O C T S R
U N W X C H W B L A T I G I D V X S J K P A I Q
M C T E C O N O M I C I M P A C T N W U B N K D
I A S P D F G A M R E J M P T Y O L T U T A A L
S T I L S S Q Y M B P M E U O I R S W I O H R I
D E L E L V I Z A K V E P N T W Y A V X M S G G
N N K V Z F H Q X U P N B C W P J K N G W X U L
A A S A B G K F M W I P A R N H R F D I W W M O
G T A L G O L K C A B R K G S M B O K W B P E B
N E T U W K M J H G T Z A Z N D Q Z P Q J P N A
I C T A K T L Q P S D N E R B D L V W E O G T L
L W N T M S O W B S A T B F S U C Q O B R E S V
E N I E U R A A W L N O I T A C I T N E H T U A
D P R O C E S S O R P A R A M E T E R S L H Y R
O P P A W J R G E O X X Y D J Z T L U A F E D I
M Z S O Y G O L O N H C E T E V I T P A D A C A
R J Q U Q V N O I T I S O P M O C E D P A S C B
Q P Z P Y O E E C A F R E T N I Q Q Y I Z M A L
Z E D I V I D L A T I G I D O I F U D B K G P E

Modeling and simulation
Boolean expression
Authentication
Abstraction
Interface
Evaluate
Digital
Input

Computation thinking
Sprint task list
Digital divide
Concatenate
Processor
Property
Binary
Event

Adaptive technology
Economic impact
Event handler
Parameters
Character
Default
Analog

Creative expression
Global variable
Decomposition
Components
Arguments
Backlog
Output